



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: JASON L. FULLER AND
SHAUN D. COMPTON

APPLICATION No.: 10/081,624

FILED: FEBRUARY 20, 2002

FOR: **MICROELECTRONIC DEVICE HAVING A
PLURALITY OF STACKED DIES AND
METHODS FOR MANUFACTURING
SUCH MICROELECTRONIC
ASSEMBLIES**

EXAMINER: JESSICA L. ROSSI

ART UNIT: 1733

CONF. No: 1950

**Supplemental Information Disclosure Statement Within Three Months
of Application Filing or Before First Action – 37 C.F.R. § 1.97(b)**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

1. Timing of Submission

This information disclosure is being filed within three months of the filing date of this application or date of entry into the national stage of an international application or before the mailing date of a first Office action on the merits, whichever occurs last [37 C.F.R. § 1.97(b)]. The references listed on the enclosed Form PTO-1449 (modified) may be material to the examination of this application; the Examiner is requested to make them of record in the application.

2. Cited Information

☒ Copies of the following references are enclosed:

- ☒ All cited references
☐ The following:

3. Effect of Information Disclosure Statement (37 C.F.R. § 1.97(h))

This Information Disclosure Statement is not to be construed as a representation that: (i) a search has been made; (ii) additional information material to the examination of this application does not exist; (iii) the information, protocols, results and the like reported by third parties are accurate or enabling; or (iv) the

cited information is, or is considered to be, material to patentability. In addition, applicant does not admit that any enclosed item of information constitutes prior art to the subject invention and specifically reserves the right to demonstrate that any such reference is not prior art.

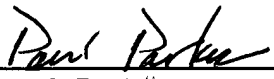
4. Fee Payment

No fees are believed due because this Information Disclosure Statement is being filed before the mailing date of the first Office Action.

However, should the Commissioner determine that fees are due in order for this Information Disclosure Statement to be considered, the Commissioner is hereby authorized to charge such fees to Deposit Account No. 50-0665.

Respectfully submitted,
Perkins Coie LLP

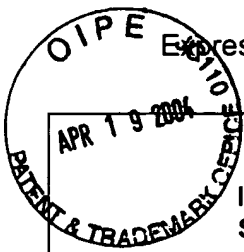
Date: April 19, 2004



Paul T. Parker
Registration No. 38,264

Correspondence Address:

Customer No. 25096
Perkins Coie LLP
P.O. Box 1247
Seattle, Washington 98111-1247
(206) 359-8000



Express Mail No. EV343595319US

Attorney Docket No. 108298636US

Disclosure No. 01-0427

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**Form PTO-1449 (Modified)
(Use several sheets if necessary)**COMPLETE IF KNOWN**

Application Number	10/081,624
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Filing Date	February 20, 2002
First Named Inventor	Jason L. Fuller
Group Art Unit	1733
Examiner Name	Jessica L. Rossi
Attorney Docket No.	108298636US

Sheet 1 of 1

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No.	U.S. Patent or Application		Name of Patentee or Inventor of Cited Document	Date of Publication or Filing Date of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		NUMBER	Kind Code (if known)			
		5,252,857		Kane et al.	10/12/1993	
		5,518,957		Kim	05/21/1996	
		5,883,426		Tokuno et al.	03/16/1999	
		6,281,577	B1	Oppermann et al.	08/28/2001	
		6,552,910	B1	Moon et al.	04/22/2003	
		6,560,117	B2	Moon	05/06/2003	

OTHER PRIOR ART-NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume issue number(s), publisher, city and/or country where published.	T
		Datacon Multichip Diebonder 2200 apm, 4 pages, Datacon Semiconductor Equipment GmbH, Radfeld/Tirol, Austria, < http://www.datacon.at >.	

EXAMINER

DATE CONSIDERED

*EXAMINER: Initial if reference considered, whether or not criteria is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to application(s).